

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Hiroshi HASHIMOTO et al.**

Group Art Unit: **2814**

Application Number: **10/083,533**

Examiner: **Thao X. Le**

Filed: **February 27, 2002**

Confirmation Number: **6400**

For: **SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
INCLUDING A SEMICONDUCTOR DEVICE HAVING A STABLE
THRESHOLD CHARACTERISTIC**

Attorney Docket Number: **020244**

Customer Number: **38834**

RESPONSE UNDER 37 C.F.R § 1.116

MAILSTOP: AF

October 5, 2007

Commissioner for Patents

P. O. Box 1450

Alexandria, VA 22313-1450

Sir:

This paper is filed in response to the Office Action dated July 6, 2007.

Remarks/Arguments begin on page 2 of this paper.